



GOA UNIVERSITY  
Taleigao Plateau, Goa 403 206

RESULT REGISTER FOR M.E ELECTRONICS & TELECOMMUNICATION (MICROELECTRONICS) SEMESTER - II EXAMINATION  
HELD IN NOVEMBER 2015  
Course : Revised Course - 2013

COLLEGE : GOA COLLEGE OF ENGINEERING

Seat No: 3201 P R No: 201004714 Sex: F Name: KANADE SHRADDHA SABAJI  
No Of Attempts : 2

2213  
16/12/16

		No Of Credits	Grade Obtained	SGPA
ASIC Design & FPGA	Theory	4	BC +	
	IA	2	BB +	
Digital Signal Processors & Embedded Systems	Theory	4	CC +	
	IA	2	BC +	
Design for Testability & E-Waste Management	Theory	4	CC +	
	IA	2	CC +	
Processor Architecture & Parallel Processing	Theory	4	BC P	
	IA	2	BC +	
Memory Design	Theory	4	BC P	
	IA	2	BC +	
Parallel Processing Lab	IA	2	BC +	
	Practical	2	AB +	
FPGA & Embedded Systems Lab	IA	2	AB +	
	Practical	2	AA +	
<b>Total:</b>		<b>38</b>		<b>6.16 P PASSES</b>

Grade	Grade Points	Performance
AO	10	Outstanding
AA	9	Excellent
AB	8	Very Good
BB	7	Good
BC	6	Fair
CC	5	Satisfactory
FF	0	Fail

Read By: *p.patel*

Checked By: *Kal*

Date: *13/12/16*

*S.S.J. Figueiredo*  
08/12/2016  
S.S.J. Figueiredo  
Assistant Registrar-E(Prof.)

*Leo V. Macedo*  
09/12/16  
Leo V. Macedo  
Controller Of Examinations

*Y.V.Reddy*  
13/12  
Prof. Y. V. Reddy  
Registrar

